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End-to-end correlated subgap states in hybrid nanowires


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End-to-end correlated bound states are investigated in superconductor-semiconductor hybrid nanowires at zero magnetic field. Peaks in subgap conductance are independently identified from each wire end, and a cross-correlation function is computed that counts end-to-end coincidences, averaging over thousands of subgap features. Strong correlations in a short, 300-nm device are reduced by a factor of 4 in a long, 900-nm device. In addition, subgap conductance distributions are investigated, and correlations between the left and right distributions are identified based on their mutual information.

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I. INTRODUCTION

Single Majorana bound states emerge at each end of a one-dimensional topological superconductor [1], and pairs of Majorana bound states have been proposed to nonlocally encode quantum information [2,3]. Following the theoretical suggestion that hybrid superconductor-semiconductor nanowires possess a topological phase [4,5], bound states within the superconducting gap (subgap states) have been extensively studied using the tunneling conductance from a single wire end, and the results are broadly consistent with Majorana modes [6–10]. It has also been discovered, both in experiment [11,12] and theory [13–19], that localized non-topological or quasi-Majorana bound states can mimic many signatures of well-separated Majorana bound states. Further, quantum-dot experiments can give information on the spatial extent of subgap states [12,20–22]. Probing both ends of the Majorana wire has been proposed to distinguish local states from Majorana modes by revealing end-to-end correlations between Majorana pairs and bulk signatures of the topological transition [19,23–27].

Independent tunneling spectroscopy of both wire ends—which is fundamentally required to assess the presence of end-to-end correlations—has so far been challenging due to an inability to noninvasively ground the wire bulk. Top-down lithography can interfere with the proximity effect [28,29], and semiconductor T-junctions have a poorly understood effect on the topological phase. The recently demonstrated selective area growth (SAG) of superconductor-semiconductor heterostructures offers an appealing solution to this problem, as it allows the lithographic processing on epitaxial contacts to be isolated from the delicate superconductor-semiconductor interface [30,31]. More broadly, SAG constitutes a potential platform for multiterminal superconductor-semiconductor devices such as qubits [32–35], Cooper-pair splitters [36], and multiterminal Josephson junctions [37–41].

In this work, we demonstrate that many of the challenges associated with studying end-to-end correlations in Majorana nanowires have been overcome. We identify subgap features at both ends of three-terminal SAG devices at zero magnetic field and quantify the correlations by averaging over thousands of subgap features. Varying the length of the superconductor-semiconductor hybrid segment, we find that correlations are reduced by a factor of 4 in a long, 900-nm nanowire as compared to a short, 300-nm nanowire. We also study the mutual information between the left and right subgap conductance distributions, finding subtle signatures of conductance correlations which are especially pronounced for a family of stable, zero-energy features. This work demonstrates an experimental protocol for quantifying end-to-end correlations and determining the associated length scales. Application of these results at a nonzero magnetic field will enable searches for correlated Majorana pairs with statistical significance.

II. EXPERIMENTAL SETUP

The platform for observing end-to-end correlations is Al-InAs SAG, grown by chemical beam epitaxy (CBE) on an InP substrate with a graded InAsP buffer layer [42], with epitaxial Al deposited in situ immediately after semiconductor growth. Following growth, Al (blue, Fig. 1) was selectively removed from the substrate and the wire ends by a wet etch, and Ti/Au Ohmic contacts (yellow Fig. 1) were deposited...
FIG. 1. (a) Scanning electron micrograph of the short device with 300-nm superconducting segment. (b) Scanning electron micrograph of the long device with 900-nm superconducting segment. (c) Conceptual sketch of the devices showing the InP substrate, SiOx mask, Al lead (blue), Ti/Au Ohmic contacts (yellow), and Ti/Au electrostatic gates (red) separated by an HfO layer. Left bias voltage \( V_L \), left measured current \( I_L \), right bias voltage \( V_R \), right measured current \( I_R \), aluminum voltage \( V_{Al} \), left-barrier voltage \( V_{LB} \), right-barrier voltage \( V_{RB} \), and plunger voltage \( V_P \) labeled.

on both ends of the wire. The device was then covered by a global HfOx dielectric before electrostatic Ti/Au gates (red Fig. 1) are deposited. Three devices were cofabricated, nominally differing only in the size of the superconducting region. The short [300 nm, Fig. 1(a)] and long [900 nm, Fig. 1(b)] devices were studied in detail. An intermediate-length device did not show evidence of superconductivity and was damaged due to electrostatic discharge; it was not studied further. All measurements were performed in a dilution refrigerator with base electron temperature \( <100 \text{ mK} \). To focus solely on the identification of end-to-end correlations without reference to the topological regime, the magnetic field is fixed to \( B = 0 \); finite-field effects are explored in a partner paper [43].

Both devices have three terminals for electrical measurement [Fig. 1(c)]. Two normal-conducting contacts were used for tunneling spectroscopy and were equipped with Basel SP983 I-to-V converters (<100 \( \Omega \) input impedance). The left normal contact sources a voltage \( V_L \) and measures a current \( I_L \). Likewise, the right normal contact sources a voltage \( V_R \) and measures a current \( I_R \). The third terminal is formed from a selective etch of the epitaxial Al and is set to a voltage \( V_{Al} \). Because the third terminal is fabricated from a subtractive process after growth, it can be formed without disrupting the fragile Al-InAs interface. Electrostatic gates, \( V_{LB} \) and \( V_{RB} \), tune the coupling to the left and right leads, and a plunger gate, \( V_P \), tunes the electrochemical potential within the wire. This setup allows the left conductance, \( g_L = \delta I_L/\delta (V_L - V_{Al}) \), and the right conductance, \( g_R = \delta I_R/\delta (V_R - V_{Al}) \), to be independently measured using lock-in techniques. The barriers are adjusted such that the above-gap conductance, averaged over gate voltage, is \( <0.15 e^2/h \).

III. RESULTS AND DISCUSSION

To study subgap structures in the short device, the right and left conductance, \( g_R \) and \( g_L \), were measured as a function of bias and plunger \( V_P \) and right bias \( V_R \). Data are from the short device. (d) Measured right conductance \( g_R \) vs plunger \( V_P \) and middle lead bias \( V_{Al} \). (e) Measured right conductance \( g_R \) vs plunger \( V_P \) and middle lead bias \( V_{Al} \). (f) Left and right conductance measured at fixed plunger voltage indicated by horizontal lines in Figs. 1(d) and 1(e). \( V_{RL,L} \) denotes \( V_L \) for \( g_L \), and \( V_R \) for \( g_R \). Data are from the long device.
of plunger gate, a characteristic of Andreev bound states. The left-side tunneling conductance [Fig. 2(b)] likewise exhibits a region of suppressed conductance at low bias, with discrete oscillating features inside the gap. The subgap structure observed on the left and right sides are similar. Directly comparing \( g_L \) and \( g_R \) at fixed gate voltage, as in Fig. 2(c), emphasizes that subgap peaks generally occur at the same bias voltage, suggesting that left and right peaks originate from the same Andreev bound state. Although peaks are generally observed at the same bias voltages, the conductance associated with these peaks does not show a qualitatively clear correspondence. On both the left and right side, the subgap conductance is not a symmetric function of bias, which is the focal point of a partner paper [43].

It is interesting to note that the low-bias features in Figs. 2(a) and 2(b) are isolated from higher-lying states, stable at zero energy, and present on both ends of the device—signatures traditionally associated with Majorana modes. We emphasize that the data are taken at \( B = 0 \), where topological effects are not generally expected [4,5].

Turning to the long device, \( g_R \) versus gate and bias exhibits a gapped low-bias conductance with discrete subgap features [Fig. 2(d)], qualitatively similar to the short device. The left-side conductance, shown in Fig. 2(e), also exhibits a gapped low-bias conductance and discrete subgap features. Unlike the short device, in the long device the left and right subgap features exhibit markedly different gate-voltage dependences. A direct comparison of \( g_L \) and \( g_R \) for the long device, shown in Fig. 2(f), confirms that subgap peaks on the left and right occur at different bias voltages, suggesting that the subgap features originate from different Andreev bound states. We note that the long-device data are acquired differently than the short-device data, by sweeping the aluminum bias \( V_{\text{AI}} \) and simultaneously measuring the currents on the left and right with \( V_{R,L} = 0 \). This method has the advantage of simultaneous data acquisition on the left and right sides but is disadvantageous in the short device, where the left and right biases must be carefully trimmed to zero to avoid complications from nonlocal effects. We have directly compared the two methods on a small subset of the data and found that they are in agreement [44]. Taken together, the observations in Fig. 2 suggest a picture where individual subgap states can extend across the short device but are associated with a single end of the long device.

To further analyze the conductance data, subgap peaks are identified using a peak-finding algorithm over a wide range of gate voltages, of which the data in Fig. 2 is a small subset. A total of 2353 peaks are extracted from the full short-device dataset, and 2058 peaks are extracted from the full long-device dataset. A plot of the identified peak locations for each plunger-gate value from Fig. 2 demonstrates that the peaks are identified accurately and emphasizes that the subgap peaks are indeed generally correlated for the short device [Fig. 3(a)] but are uncorrelated for the long device [Fig. 3(b)].

The correlations are quantified by introducing binary masks for the left side, \( B_L(V, V_P) \), and the right side, \( B_R(V, V_P) \), which take a value of 1 if a peak is identified at bias voltage \( V \) and plunger voltage \( V_P \) and a value of 0 otherwise. The cross-covariance, \( C \), between the left and right binary masks is then computed, allowing for a bias offset, \( \delta V \), between the two sides:

\[
C(\delta V) = \langle B_L(V, V_P)B_R(V + \delta V, V_P) \rangle - \langle B_L(V, V_P) \rangle \langle B_R(V, V_P) \rangle,
\]

where \( \langle \cdot \rangle \) denotes the expectation value with respect to both \( V \) and \( V_P \). \( C \) serves as a correlation metric by counting the number of coincident peaks in each bias trace, averaged over plunger voltage.

In the short device, \( C \) is peaked at around \( \delta V = 0 \). Including only the subset of data from Fig. 3(a) results in a correlator that is significantly less than unity [dashed trace, Fig. 3(c)], indicating that on average less than one peak is identified as being correlated at each gate voltage. This is a result of the stringent definition of \( C \); it counts peaks as correlated only if they occur at identical bias voltages. Indeed, inspection of the data in Fig. 3(a) reveals that every peak has a nearby partner, while only some occur at identical parameters. Computing \( C \) for the full short-device dataset [solid trace, Fig. 3(c)] increases the average number of coincident subgap peaks to \( C = 0.97 \). For large bias shifts, the correlator \( C \) has small fluctuations, consistent with the background level inferred by introducing a gate-voltage shift between the datasets.

In contrast, for the long device, no significant correlations are observed when including only the subset of the data in
PNMI, where the overline denotes an expectation value. VR device subgap states \( f \).

Information (PNMI), for short-device subgap states \( e \) and for long-device subgap states \( g \). The observed drop in correlations, associated with these features fluctuates strongly. To study the distribution as a function of bias shift \( \delta V \), the mutual information decays with a \( 1/e \) characteristic length that approximately matches the observed width of subgap peaks, suggesting that the conductance correlations are associated with subgap states. As a function of plunger shift \( \delta V_p \), the mutual information also decays sharply, again consistent with contributions from subgap states. In contrast, for the long device, the mutual information is relatively flat and featureless [Fig. 4(d)], suggesting that there are no conductance correlations originating from the subgap states. The mutual information for the long device, however, still differs significantly from zero, indicating there are broad gate- and bias-voltage independent features that are mutually dependent between the two ends.

A more granular view of the conductance correlations is obtained by visualizing the pointwise normalized mutual information (PNMI), which is defined by the relation

\[
\text{PNMI} = \text{NMI}(\delta V, \delta V_p)
\]

For the short device, the mutual information is strongly peaked at \( \delta V = \delta V_p = 0 \) [Fig. 4(c)], indicating the presence of correlations in the conductance distributions. As a function of bias shift \( \delta V \), the mutual information decays with a \( 1/e \) characteristic length that approximately matches the observed width of subgap peaks, suggesting that the conductance correlations are associated with subgap states. As a function of plunger shift \( \delta V_p \), the mutual information also decays sharply, again consistent with contributions from subgap states. In contrast, for the long device, the mutual information is relatively flat and featureless [Fig. 4(d)], suggesting that there are no conductance correlations originating from the subgap states. The mutual information for the long device, however, still differs significantly from zero, indicating there are broad gate- and bias-voltage independent features that are mutually dependent between the two ends.

IV. CONCLUSIONS

We interpret the well-correlated subgap states in the short device as a clear indication that bound states extend several hundred nanometers into the hybrid region, essentially spanning the entire device. If the zero-field bound states are attributed to an accidental potential well in the tunneling region [9], then their spatial extent would indicate a soft confinement, which will strongly affect the behavior at both sides in the short device as compared to the long device, which gives a preliminary indication of positive correlations in conductance for the short device.
nonzero magnetic field [13,14,17]. Following this reasoning, we anticipate that these measurements could be used to refine the theoretical understanding of topologically trivial subgap states, including in numerical simulations, and possibly aid in distinguishing them from the topological case.

Looking ahead, the techniques developed here can be applied in the putative topological regime. Computing a correlator after feature extraction, as in Fig. 3, effectively removes the background from conductance fluctuations. Quantifying conductance correlations using the mutual information requires minimal analysis of the data and explicitly tests for correlations in the conductance values of all data points.

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